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Terms	Documents
L26 and (partition\$ same hardware\$ same (firmware\$ or operating system))	1

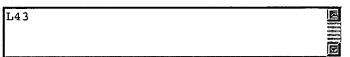
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<u>L43</u>	126 and (partition\$ same hardware\$ same (firmware\$ or operating system))	1	<u>L43</u>
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<u>L41</u>	126 and (debug\$ near6 partition\$)	1	<u>L41</u>
<u>L40</u>	124 and (partition\$ or divi\$ or separat\$ or cell\$ or segment\$)	1	<u>L40</u>
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<u>L34</u>	124 and switch\$	1	<u>L34</u>
<u>L33</u>	l24 and (enabl\$ or disabl\$)	1	<u>L33</u>
<u>L32</u>	124 and read\$	1	<u>L32</u>

<u>L31</u>	129 and (exception\$ or notifi\$ or fault\$)	1	<u>L31</u>
<u>L30</u>	124 and report\$ and event\$	0	<u>L30</u>
<u>L29</u>	124 and event\$	1	<u>L29</u>
<u>L28</u>	124 and (call\$ or invok\$) and execut\$	1	<u>L28</u>
<u>L27</u>	L26 and (partition\$ near9 (memory\$ or firmware\$ or rom or bios\$))	1	<u>L27</u>
<u>L26</u>	6684343.pn.	1	<u>L26</u>
<u>L25</u>	L24 and extension\$	1	<u>L25</u>
<u>L24</u>	5889988.pn.	1	<u>L24</u>
<u>L23</u>	121 and (partition\$ or divi\$ or segment\$)	0	<u>L23</u>
<u>L22</u>	L21 and extension\$ and debug\$ and operating system\$	1	<u>L22</u>
<u>L21</u>	5958049.pn.	1	<u>L21</u>
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<u>L18</u>	L16 and partition\$ near6 (firmware\$ or memory\$ or rom\$ or bios)	1	<u>L18</u>
<u>L17</u>	L16 and partition\$	1	<u>L17</u>
<u>L16</u>	6381682.pn.	1	<u>L16</u>
<u>L15</u>	L14 and operating system	7	<u>L15</u>
<u>L14</u>	L13 and debug\$	7	<u>L14</u>
<u>L13</u>	hypervisor\$	118	<u>L13</u>
<u>L12</u>	(debug\$ near4 hypervisor\$)	0	<u>L12</u>
<u>L11</u>	L10 and debug\$	4	<u>L11</u>
<u>L10</u>	(partition near6 firmware\$)	33	<u>L10</u>
<u>L9</u>	(debug\$ of test\$) near5 (partition\$ or divi\$) near5 (firmware\$ or rom or memory\$ or bios\$)	0	<u>L9</u>
<u>L8</u>	(debug\$ of test\$)near5 (partition\$ or divi\$) near5 (firmware\$ or rom or memory\$ or bios\$)	0	<u>L8</u>
<u>L7</u>	debug\$ near5 (partition\$ or divi\$) near5 firmware	0	<u>L7</u>
<u>L6</u>	11 and (partition\$ or divis\$) near5 (firmware\$ or bios\$ or rom or memory)	1	<u>L6</u>
<u>L5</u>	12 and (partition\$ or divis\$) near5 (firmware\$ or bios\$ or rom or memory)	0	<u>L5</u>
<u>L4</u>	L3 and (file system extension program)	1	<u>L4</u>
<u>L3</u>	6289448.pn.	1	<u>L3</u>
<u>L2</u>	L1 and (operating system\$ near5 debug\$)	18	<u>L2</u>
<u>L1</u>	extension\$ near5 debug\$	98	<u>L1</u>

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<u>L3</u>	711/153,170,149.ccls.	1344	<u>L3</u>
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1 A relational approach to monitoring complex systems Richard Snodgrass

May 1988 ACM Transactions on Computer Systems (TOCS), Volume 6 Issue 2

Full text available: pdf(3.42 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

Monitoring is an essential part of many program development tools, and plays a central role in debugging, optimization, status reporting, and reconfiguration. Traditional monitoring techniques are inadequate when monitoring complex systems such as multiprocessors or distributed systems. A new approach is described in which a historical database forms the conceptual basis for the information processed by the monitor. This approach permits advances in specifying the low-level data collection, ...

² Illustrative risks to the public in the use of computer systems and related technology Peter G. Neumann



January 1996 ACM SIGSOFT Software Engineering Notes, Volume 21 Issue 1

Full text available: pdf(2.54 MB) Additional Information: full citation

Design challenges of virtual networks: fast, general-purpose communication Alan M. Mainwaring, David E. Culler



May 1999 ACM SIGPLAN Notices, Proceedings of the seventh ACM SIGPLAN symposium on Principles and practice of parallel programming, Volume 34 Issue

Full text available: pdf(1.57 MB)

Additional Information: full citation, abstract, references, citings, index terms

Virtual networks provide applications with the illusion of having their own dedicated, highperformance networks, although network interfaces posses limited, shared resources. We present the design of a large-scale virtual network system and examine the integration of communication programming interface, system resource management, and network interface operation. Our implementation on a cluster of 100 workstations quantifies the impact of virtualization on small message latencies and throughput ...

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Debuggable concurrency extensions for standard ML

Andrew P. Tolmach, Andrew W. Appel

December 1991 ACM SIGPLAN Notices, Proceedings of the 1991 ACM/ONR workshop on Parallel and distributed debugging, Volume 26 Issue 12

Full text available: pdf(1.22 MB)

Additional Information: full citation, references, citings, index terms

Procs and locks: a portable multiprocessing platform for standard ML of New Jersey J. Gregory Morrisett, Andrew Tolmach



July 1993 ACM SIGPLAN Notices, Proceedings of the fourth ACM SIGPLAN symposium on Principles and practice of parallel programming, Volume 28 Issue 7

Full text available: pdf(976.70 KB)

Additional Information: full citation, abstract, references, citings, index terms

We have built a portable platform for running Standard ML of New Jersey programs on multiprocessors. It can be used to implement user-level thread packages for multiprocessors within the ML language with first-class continuations. The platform supports experimentation with different thread scheduling policies and synchronization constructs, it has been used to construct a Modula-3 style thread package and a version of Concurrent ML, and has been ported to three different mu ...

Composing first-class transactions

Nicholas Haines, Darrell Kindred, J. Gregory Morrisett, Scott M. Nettles, Jeannette M. Wing November 1994 ACM Transactions on Programming Languages and Systems (TOPLAS), Volume 16 Issue 6

Full text available: pdf(1.07 MB)

Additional Information: full citation, references, citings, index terms, review

Keywords: Standard ML, modules, persistence, recovery, serializability, skeins, threads, transactions, undoability

A bibliography of parallel debuggers, 1993 edition

Cherri M. Pancake, Robert H. B. Netzer

December 1993 ACM SIGPLAN Notices, Proceedings of the 1993 ACM/ONR workshop on Parallel and distributed debugging, Volume 28 Issue 12